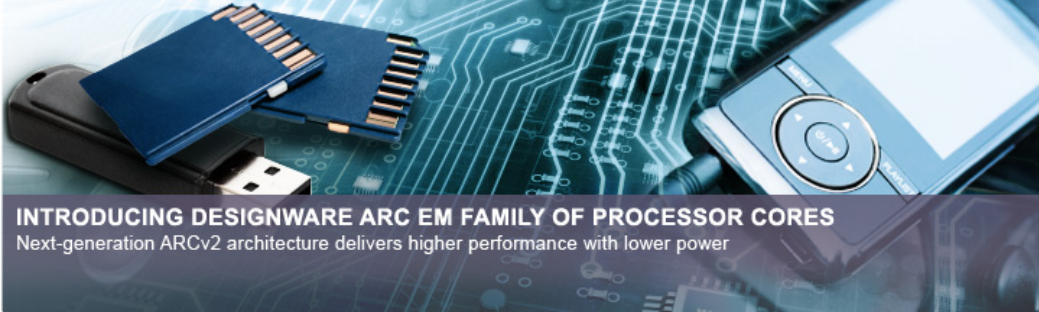




## DesignWare ARC EM Processor Core Family



### INTRODUCING DESIGNWARE ARC EM FAMILY OF PROCESSOR CORES

Next-generation ARCV2 architecture delivers higher performance with lower power

#### CUSTOMER SUCCESS

Siano Achieves First-Pass Silicon Success with DesignWare ARC Processor Core

#### PRESS RELEASE

Synopsys Introduces Starter Kit for ARC EM Processors

#### WEBINAR

3 Easy Ways to Accelerate Development of your Embedded SoC

#### ARTICLE

ARC EM Starter Kit Accelerates Embedded Software Development

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### Unrivalled Performance Efficiency for Your Embedded Application

The DesignWare® ARC® EM Family of embedded processor cores is based on the scalable ARCV2 Instruction Set Architecture (ISA) and is optimized for performance efficiency (DMIPS/mW and DMIPS/mm2). The ARC EM family includes the EM4 (cacheless) and EM6 (instruction and data caches) processor cores, designed for use in power and area-sensitive embedded applications. They offer industry-leading performance efficiency of up to 1.71 DMIPS/MHz, with minimal area and power consumption.

The EM Processors are highly-configurable and extensible, enabling designers to implement each core with the optimum combination of performance, code density, area and power consumption for the specific task or application.

The EM Family of processor cores is supported by a robust ecosystem of software and hardware development tools, including an easy to use and low-cost [ARC EM Starter Kit](#) for early software development, the MQX real-time operating system (RTOS), and a portfolio of third-party tools, operating systems and middleware from leading industry vendors through the [ARC Access Program](#).

[Register for ARC EM Processor Online Training](#)

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#### Products

- ARC EM4** Compact, ultra low-power 32-bit processor for deeply embedded designs [MORE](#)
- ARC EM6** General-purpose, embedded 32-bit processor core with I & D cache [MORE](#)

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