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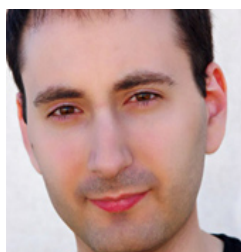
Intel 2012 Doctoral Student Honor Awardees

Recognizing future technology leaders



Intel Announces the 2012 Doctoral Student Honor Awardees

The Intel Doctoral Student Honor Programme awards fellowships to exceptional PhD candidates pursuing leading-edge innovation in fields related to Intel's business and research interests in the European Union, Switzerland, and Russia. The goal of the programme is to advance innovation in key areas of technology, as well as develop a pipeline of world-class technical talent for Intel's future workforce and the global knowledge-based economy. The selection of this year's recipients was a highly competitive process with many outstanding quality applicants across several universities and exciting areas of research. For the 2012-2013 academic cycle, twenty-five finalists were selected from a pool of sixty-six applicants across sixteen universities. Congratulations to all of this year's awardees!



Jose Maria Arnau, U. Polit. De Catalunya

Thesis: Energy-Efficient Mobile GPU Systems for Smartphones and Tablets



Jeronimo Castrillon, RWTH Aachen

Thesis: Methodologies and Algorithms for Efficient Execution of Multiple Applications on Heterogeneous MPSoCs



Jose David Domenech Gomez, U. Polit. De Valencia

Thesis: Apodized Coupled Resonator Optical Waveguides: Theory, Design and Characterization



Jesus Friginal Lopez, U. Polit. De Valencia

Thesis: An Experimental Methodology to Evaluate the Resilience of Ad Hoc Routing Protocols



Shrikanth Ganapathy, U. Polit. De Catalunya

Thesis: Reliability in the Face of Variability in Nanometer Caches



Carmen Garcia, U. Polit. De Catalunya

Thesis: Future Technologies and New Computing Paradigms



Sven Gehring, Saarland University

Thesis: Interaction with Media Facades



Manuel Gorius, Saarland University

Thesis: Adaptive Real-Time Internet Media Transport: Capacity-Approaching Streaming Over Open Internet



Alexander Heinecke, TU Munchen

Thesis: Multi- and Manycore Architectures in Advanced Scientific Computing



Alec Jacobson, ETH Zurich

Thesis: Algorithms and Interfaces for Real-Time Deformation of 2D and 3D Shapes



Gareth Jones, Imperial College London

Thesis: Performance Analysis

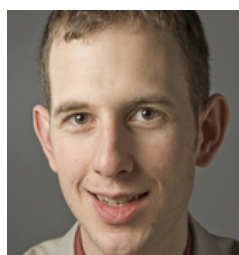


Daniel Kelly, Imperial College London

Thesis: Disaggregation of



Pejman Lotfi Kamran, Ecole Polytechnique Federale de Lausanne (EPFL)



Pascal Meinerzhagen, Ecole Polytechnique Federale de Lausanne



Bojan Milosevic, University of Bologna

Thesis: Energy Efficient Body

Thesis: Performance Analysis
with Fluid Queues



Bharghava Rajaram,
University of Edinburgh

Thesis: Redefining Atomicity
for Performance and
Portability

Thesis: Disaggregation of
Low Temporal Resolution
Smart Meter Data



Pablo Reble, RWTH Aachen

Thesis: Design Patterns for
Scalable Synchronization of
Manycore Processors

Thesis: Scale Out Processors



Michele Rossi, University of
Bologna

Thesis: Electronic Interfaces
for Nanosensors

Thesis: Alternative
Approaches toward Area- and
Energy-Efficient Embedded
Memories



Lisa Rutledge, University
College Dublin

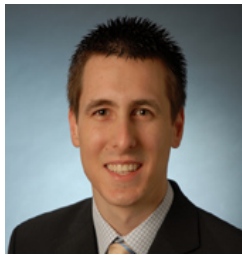
Thesis: Wind Power:
Enhancing Short-Term Power
System Flexibility

Thesis: Energy Efficient Body
Area Networks for Motion
Tracking and Analysis



Reinhard Schneider, TU
Munchen

Thesis: Architecture-Aware
Co-Design of Automotive
Software Systems: A Cyber-
Physical Systems Approach



Lars Schor, ETH Zurich

Thesis: Programming and
Optimization Environment for
Predictable and Efficient
Embedded Manycore Systems



Aonghus Shortt, University
College Dublin

Thesis: Quantifying and
Responding to Power System
Variability



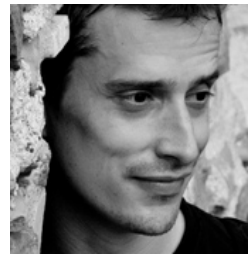
Evgeny Strekalovskiy, TU
Munchen

Thesis: Convex Optimization
Methods for Computer Vision



Alejandro Valero, U. Polit.
De Valencia

Thesis: Energy-Aware Hybrid
SRAM/eDRAM Caches and
Architectural Strategies



Cristian Zamfir, Ecole
Polytechnique Federale de
Lausanne

Thesis: Execution Synthesis:
A Technique for Automated
Software Debugging

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